		Application/Control No.	Applicant(s)/Patent Under Reexamination
Notice of References Cited		10/772,555	SMITH ET AL.
		Examiner	Art Unit	
		BEN C. WANG	2192	Page 1 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Quinn Able Jacobson, "High-Performance Frontends For Trace Processors", 1999, Univ. of Wisconsin - Madison, pp. 1-225
	V	Ashutosh Sham Dhodapkar, "Autonomic Management of Adaptive Microarchitectures", 2004, Univ. of Wisconsin - Madison, pp. 1-154
	W	Smith et al., "Dynamic Microarchitecture Adaptation via Co-Designed Virtual Machines", ISSCC 2002, 2002 IEEE, pp. 1-2
	X	Dhodapkar et al., "Comparing Program Phase Detection Techniques", Dec. 2003, IEEE, pp. 1-11

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

		Notice of References Cited	Application/Control No. 10/772,555	Applicant(s)/Patent Under Reexamination SMITH ET AL.	
			Examiner BEN C. WANG	Art Unit 2192	Page 2 of 2

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Dhodapkar et al., "Tuning Reconfigurable Microarchitectures for Power Efficiency", 2004 IEEE, pp. 1-7
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.